

WSF07N20

N-Ch MOSFET

General Description

The WSF07N20 is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSF07N20 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

Product Summery

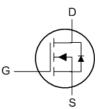
BVDSS	RDSON	ID
200V	0.49Ω	7A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch

TO-252 Pin Configuration





Symbol Parameter Rating Units v V_{DS} Drain-Source Voltage 200 Gate-Source Voltage V V_{GS} ± 30 I_D@T_C=25℃ Continuous Drain Current, V_{GS} @ 10V¹ 7 А Continuous Drain Current, V_{GS} @ 10V¹ 3.24 A I_D@T_C=100℃ Continuous Drain Current, V_{GS} @ 10V¹ I_D@T_A=25℃ 6 А Continuous Drain Current, V_{GS} @ 10V¹ I_D@T_A=70℃ 4.5 А Pulsed Drain Current² 20 А I_{DM} Single Pulse Avalanche Energy³ EAS 125 mJ Avalanche Current 8 А IAS P_D@T_C=25℃ Total Power Dissipation³ 78 W P_D@T_c=100℃ Total Power Dissipation³ 45 W °C $\mathsf{T}_{\mathsf{STG}}$ Storage Temperature Range -55 to 150 **Operating Junction Temperature Range** -55 to 150 °C T_J

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit	
R _{0JA}	Thermal Resistance Junction-ambient ¹		30	°C/W	
R _{eJC}	Thermal Resistance Junction-Case ¹		1.6	°C/W	

Absolute Maximum Ratings



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Electrical Characteristics (T_J=25¹C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	200			V
$\triangle BV_{DSS} / \triangle T_J$	BVDSS Temperature Coefficient	Reference to $25^\circ\!\mathrm{C}$, I_D=1mA		0.25		V/℃
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =2.5A		0.49	0.58	Ω
		V _{GS} =6.0V , I _D =1.9A		0.52	0.61	Ω
V _{GS(th)}	Gate Threshold Voltage		2.0	3.0	4.0	V
	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, I _D =250uA		-4.64		mV/℃
	Drain-Source Leakage Current	V _{DS} =200V , V _{GS} =0V , TJ=25℃			1	
I _{DSS}		V _{DS} =160V , V _{GS} =0V , T _J =125℃			10	uA
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm30V$, $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V _{DS} =30V , I _D =2.5A		5.2		S
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		2	4	Ω
Qg	Total Gate Charge (10V)	V _{DS} =160V , V _{GS} =10V , I _D =5A		10.8		
Q _{gs}	Gate-Source Charge			1.7		nC
Q _{gd}	Gate-Drain Charge			3.1		
T _{d(on)}	Turn-On Delay Time			7.33		
Tr	Rise Time	V _{DD} =100V , V _{GS} =10V ,		10.7		
T _{d(off)}	Turn-Off Delay Time	R _G =10Ω I _D =5A R _L =10Ω		18.2		ns
T _f	Fall Time			11.9		
Ciss	Input Capacitance	V _{DS} =30V , V _{GS} =0V , f=1MHz	255	300	360	
C _{oss}	Output Capacitance			30.2		pF
C _{rss}	Reverse Transfer Capacitance			2.3]

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V , L=0.1mH , I _{AS} =5A	100			mJ

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,6}				5	А
I _{SM}	Pulsed Source Current ^{2,6}	$V_G = V_D = 0V$, Force Current			20	А
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =5A , T _J =25℃			1.4	V
t _{rr}	Reverse Recovery Time			125.5		nS
Qrr	Reverse Recovery Charge	l₣=5A , dl/dt=100A/µs , T _J =25℃		357		nC

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper,t<10sec.

2.The data tested by pulsed , pulse width $\,\leq\,$ 300us , duty cycle $\,\leq\,$ 2%

3. The EAS data shows Max. rating . The test condition is $V_{\text{DD}}\text{=}25\text{V}, V_{\text{GS}}\text{=}10\text{V}, \text{L=}0.1\text{mH}, I_{\text{AS}}\text{=}5\text{A}$

4. The power dissipation is limited by 150°C junction temperature

5. The Min. value is 100% EAS tested guarantee.

6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



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Typical Characteristics

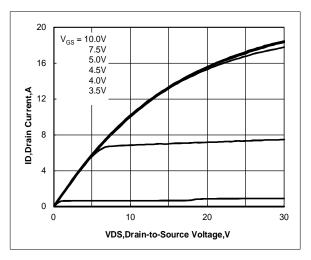


Figure 1. Output Characteristics

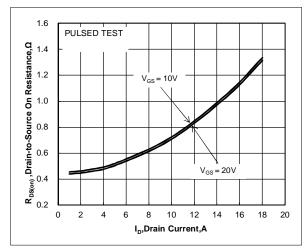


Figure 3. Drain-to-Source On Resistance vs. Drain Current and Gate Voltage

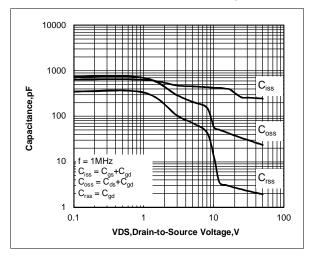


Figure 5. Capacitance Characteristics

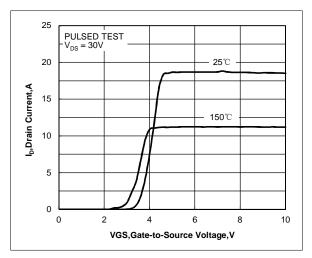


Figure 2. Transfer Characteristics

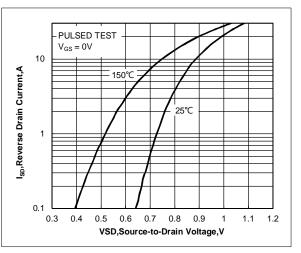


Figure 4. Body Diode Forward Voltage vs. Source Current and Temperature

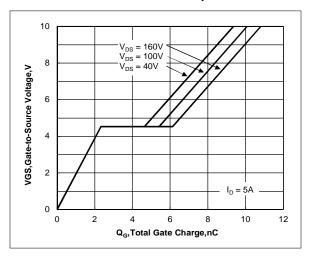
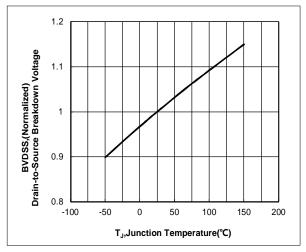


Figure 6. Gate Charge Characteristics



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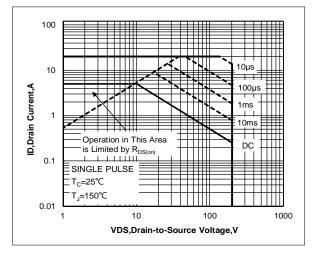


Figure 9. Maximum Safe Operating Area for RU5N20A

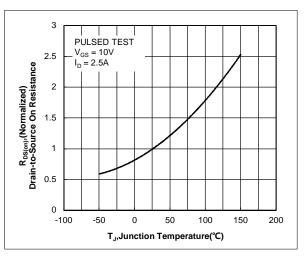


Figure 8. Normalized On Resistance vs. Junction Temperature

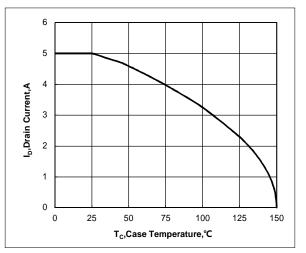
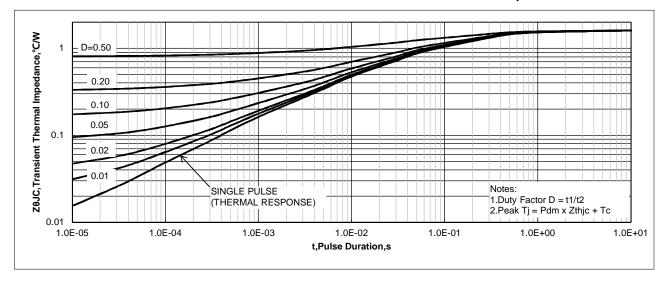


Figure 10. Maximum Continuous Drain Current vs. Case Temperature





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